

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,057,257 B2  
APPLICATION NO. : 10/071453  
DATED : June 6, 2006  
INVENTOR(S) : Tran

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page item (54), please delete the following title, **“SEMICONDUCTOR PROCESSING METHODS OF FORMING TRANSISTORS, SEMICONDUCTOR PROCESSING METHODS OF FORMING DYNAMIC RANDOM ACCESS MEMORY CIRCUITRY, AND RELATED INTEGRATED CIRCUITRY”** and insert -- **PLURALITY OF TRANSISTORS HAVING DIFFERENT ACTIVE AREA WIDTHS AND DIFFERENT THRESHOLD VOLTAGES DEFINED BY STI--**.

Title page 2 item (56), U.S. PATENT DOCUMENTS, please insert --6,107,134 \* 8/2000 Lu et al.-- before “6,121,651”.

Title page 2 item (56), U.S. PATENT DOCUMENTS, second column, please insert --6,297,082 \* 10/2001 Line et al.-- before “6,121,651”.

Title page 2 item (56), U.S. PATENT DOCUMENTS, second column, please delete “2002/0020106” after “Berry et al. ....257/397” and insert --2003-0020106--.

Col. 1, line 63, please delete “is” after “access”.

Col. 4, line 66, please delete “ $V_{tl}$ ,” before “while” and insert -- $V_{tl}$ , --.

Col. 5, line 6, please delete “ $V_{tl}$ ,” after “voltage” and insert -- $V_{tl}$ , --.

Col. 5, line 21, please delete “CSAL” after “signal” and insert -- $C_{SAL}$ --.

Col. 5, line 42, please delete “ca” after “tion” and insert --can--.

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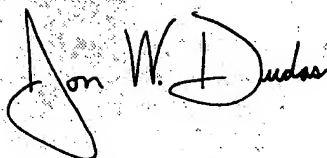
Page 2 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 6, line 32, claim 6, please delete " $V_{tl}$ ," after "voltage" and insert -- $V_{tl}$ --.

Signed and Sealed this

Twenty-eighth Day of August, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas", is written over a faint, circular embossed seal of the United States Patent and Trademark Office.

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*